



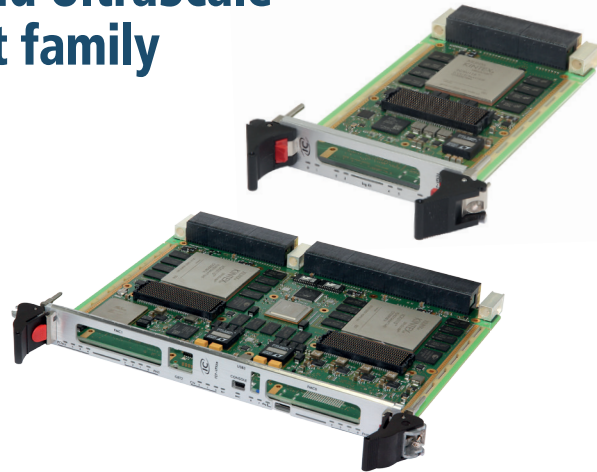
**INTERFACE CONCEPT**  
ADVANCED ELECTRONIC SOLUTIONS

## New Xilinx UltraScale™ and UltraScale™+ + FPGAs VPX COTS product family

Following the success of its Xilinx® Virtex6®/ Virtex7® FPGA VPX boards, Interface Concept expands its Front-End FPGA VPX processing board family with the **IC-FEP-VPX3d** (3U) and **IC-FEP-VPX6e** (6U) boards based on the new Xilinx® UltraScale™ and UltraScale™+ FPGA devices.

These boards benefit from the latest enhancements of Xilinx 20nm technology (Performance, Power dissipation, Scalability) and offer new features :

- FMC+ site(s) compliant with the VITA 57.4 standard
- New DDR4 technology
- PCIe gen3 and 10GigaE IP cores implementation
- Introduction of the latest QorIQ® LS1046A processor - quad 64-bit Arm® Cortex A72 cores@1,8Ghz (on the VPX6U form-factor only)



IC firmware (BSP and example design) and technical support allow an efficient and fast integration with IC other product ranges : ADC/DAC FMC, Intel and PowerPC DSP and SBC, Ethernet Switches etc.

These **IC-FEP VPX3d** and **IC-FEP-VPX6e** are aimed at high performance applications that require high-computing needs (radar, electronic warfare...)



**Please consult us for any further information needed on these products.**

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